

Serial Number

09/184, 419

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Art Unit: PUBLICATIONS
Examiner: Ms. Patricia Small
Phone: (703) 305-0333 x142
From: Sheila R. Harter

Re: Application No. 09/784,419; Filed February 15, 2001
Entitled LINEAR FULL RATE PHASE DETECTOR AND CLOCK DATA
RECOVERY CIRCUIT
File: 50987/CTD/B600

Thank you for your time and assistance this afternoon. Pursuant to your request, I have attached copies of the following: 1) *Substitution of Attorney with Change of Address for Correspondence by Assignee filed September 29, 2003* and 2) *Status Inquiry* requesting the status of the Substitution of Attorney filed on **August 30, 2004**. As we discussed during our telephone conversation, I await your response. My direct line is (626) 683-5942.

I HEREBY CERTIFY THAT THESE PAPERS ARE BEING FACSIMILE
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Sheila Harter

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Christie, Parker & Hale, LLP
350 West Colorado Boulevard
Post Office Box 7068
Pasadena, CA 91109-7068
626-795-9900
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CCCHRISTIE, PARKER & HALE

002/012

Linear Multi Stage Phase
Selection and Hatch

Ref/Ref No:

ed/Assignor

0-15-01

Client ID 15400
Case No 10418
Atty/Sec D13 P4
Date Mailed 04/03/05
Date Due 04/03/05
Cert of Mailing Yes
Express Mail No.

— Assigned Enclosed (List Assignee)

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(50987-DBP/B600

PATENT

ART UNIT 2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No. 30/DBP/B600

**SUBSTITUTION OF ATTORNEY WITH
CHANGE OF ADDRESS FOR CORRESPONDENCE
BY ASSIGNEE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Commissioner:

Broadcom Corporation, assignee of the entire interest in and to each U.S. patent application, identified on Exhibit A attached, under an Assignment recorded in the U.S. Patent and Trademark Office, hereby revokes all previous Powers of Attorney and appoints:

D. Bruce Prout	(20,958)	Harold E. Wurst	(22,183)	Jun-Young R. Jeon	(43,693)
Richard J. Ward, Jr.	(24,187)	Robert A. Green	(28,301)	Peter A. Nichols	(47,822)
LeRoy T. Rahn	(20,356)	Richard A. Wallen	(22,671)	Stephen D. Burbach	(40,285)
Walter G. Maxwell	(25,355)	Michael J. MacDermott	(29,946)	Mark J. Marcelli	(36,593)
William P. Christie	(29,371)	Anne Wang	(36,045)	David J. Steele	(47,317)
David A. Dillard	(30,831)	Constantine Manantidis	(39,759)	John W. Peck	(44,284)
Thomas J. Daly	(32,213)	Daniel R. Kimbell	(34,849)	Tom H. Dao	(44,641)
Vincent G. Giopia	(19,959)	Daniel M. Cavanagh	(41,661)	Frank L. Cre	(42,419)
Edward R. Schwartz	(31,135)	Gary J. Nelson	(44,257)	Rodney V. Warfford	(51,304)
John D. Carpenter	(34,133)	Kathleen M. Olster	(42,052)	R. W. Johnston	(17,968)
Wesley W. Monroe	(39,778)	Josaphine E. Chang	(46,083)	Hayden A. Carney	(22,653)
David A. Plumley	(37,208)	Joel A. Kauth	(41,886)	Russell R. Palmer, Jr.	(22,994)
Gregory S. Lampert	(35,581)	Patrick Y. Ikehara	(42,681)	Richard D. Seibel	(22,134)
Mark Garcia	(31,953)	Raymond R. Tabandeh	(43,945)	Richard J. Paciulan	(28,248)
Syed A. Hasan	(41,057)	Cynthia A. Bonner	(44,548)	Richard A. Clegg	(33,485)

all members or associates of or of counsel to the firm CHRISTIE, PARKER & HALE, LLP, telephone (626) 795-9900, as principal attorneys with power to appoint associate attorneys, to prosecute this application and any subsequent application based on the disclosure of this application, and to transact all business in the Patent and Trademark Office connected with this application and any subsequent application.

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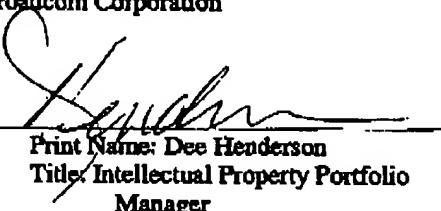
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Please address all correspondence to **CHRISTIE, PARKER & HALE, LLP, P.O. Box 7068, Pasadena, California 91109-7068**. Customer Number: 23363

Broadcom Corporation

By


Print Name: Dee Henderson
Title: Intellectual Property Portfolio Manager

DBP/djp

DJP PASS24724.1 - 09/5/03 9:47 AM

CPH/BP No. Application No. Filing Date Title

/BP1646	09/614,308	7/12/2000	<u>Fast Acquisition Phase Locked Loop Using a Current DAC</u>
50944/BP1647	09/540,243/ 6,526,113	3/31/2000/ 2/25/03	<u>GM Cell Based Control Loops</u>
/BP1648	09/632,665	8/7/2000	<u>CMOS Lock Detect With Double Protection</u>
50955/BP1649	09/632,666	8/7/2000	<u>Built-In Shelf Test for Multi-Channel Transceivers without Data Alignment</u>
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/BP1651CON	10/095,556/ 6,549,599	3/11/2002/ 4/15/2003	<u>Stable Phase Locked Loop Having Separated Pole</u>
50952/BP1653	09/792,684/ 6,566,971	2/24/01/ 5/20/2003	<u>Method and Circuitry for Implementing a Differentially Tuned Varactor-Inductor Oscillator</u>
50953/BP1653CON	10/407,909	4/2/2003	<u>Method and Circuitry for Implementing a Differentially Tuned Varactor-Inductor Oscillator</u>
/BP1654	09/782,687	2/12/2001	<u>Linear Half-Rate Phase Detector and Clock and Data Recovery Circuit</u>
/BP1655	09/784,419	2/15/2001	<u>Linear Full-Rate Phase Detector and Clock and Data Recovery Circuit</u>
50947/BP1656	09/772,781/ 6,396,894	1/29/2001/ 5/28/2002	<u>Overflow Detector for FIFO</u>
50948/BP1656CON1	10/104,870/ 6,519,311	3/21/2002/ 2/11/2003	<u>Overflow Detector for FIFO</u>
/BP1657	09/788,220/ 6,396,360	2/16/2001/ 5/28/2002	<u>Method and Circuitry for Implementing an Inductor-Capacitor Phase Interpolated Voltage-Controlled Oscillator</u>
/BP1658	09/650,275	8/29/2000	<u>Seal Ring for Integrated Circuits</u>
/BP1660	09/919,636	7/31/2001	<u>Fully Differential CMOS Phase-Locked Loop</u>
/BP1815	09/860,284	5/18/2001	<u>Varactor Based Differential VO Band Switching</u>
50957/BP1818	10/037,897	10/22/2001	<u>Methods and Circuitry for Reducing Intermodulation in Integrated Transceivers</u>

EXHIBIT A

Page 1

CPH/BP No.	Application No.	Filing Date	Title
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/BP1851	09/927,705	8/10/2001	Line Loop Back for Very High Speed Application
50950/BP1883DIV	10/267,054	10/7/2002	Low Voltage Differential to Single-Ended Converter
/BP1885	09/910,436	7/19/2001	Synchronous Data Serialization Circuit
/BP1885CON	10/431,103	5/6/2003	Synchronous Data Serialization Circuit
50958/BP1884	09/955,693	9/18/2001	Linear Phase Detector for High Speed Clock data Recovery
50945/BP2015	09/969,837	10/1/2001	High-Speed Peak Amplitude Comparator
/BP2137	10/159,788	5/30/2002	Method and Apparatus for High Speed Signal Recovery
50956/BP2138	10/092,166	3/4/2002	High Frequency Statistical Loss of Signal Detector
50964/BP2144	10/241,140	9/10/2002	Phase Lock Loop with Cycle Drop and Add Circuitry
50963/BP2233	10/243,086	9/12/2002	Delay Generator
50959/BP2233.1	10/243,281	9/12/2002	Symmetric Differential Logic Circuits
50960/BP2360	10/293,163	11/12/2002	Phase Detector for Extended Linear Response and High-Speed Data Regeneration
50961/BP2361	10/293,624	11/12/2002	Phase Detector with Delay Elements for Optimum Data Regeneration
50962/BP2365	10/335,190	12/30/2002	CDR Lock Detector with Hysteresis
50896/BP2454	09/747,392	12/22/2000	Methods of Recording Voice Signals in a Mobile Set
/BP2445	09/640,963	8/16/2000	Code Puncturing Method and Apparatus
/BP2446	09/636,000	8/9/2000	Maximum Likelihood Sequence Estimator which Computes Branch Metrics in Real Time
/BP2448	10/228,165	8/26/2002	Frequency Offset Correction Circuit for WCDMA
/BP2449	10/242,319	9/11/2002	MPSK Equalizer
/BP2450	10/272,507	10/15/2002	Shifted 8PSK Modulator for EDGE

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EXHIBIT A
Page 2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jun Cao
Application No. : 09/784,419
Filed : February 15, 2001
Title : LINEAR FULL RATE PHASE DETECTOR
AND CLOCK DATA RECOVERY CIRCUIT

Grp./Div. : 2633
Examiner : David C. Payne

Docket No. : 50987/RJP/B600

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
August 30, 2004

Commissioner:

This is to inquire as to the status of the Substitution of Attorney with Change of Address for Correspondence by Assignee filed on September 29, 2003, a copy of which is enclosed. Applicant respectfully requests confirmation of this submission.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By Colin Dorrian
Colin Dorrian
Reg. No. 54,658
626/795-9900

CTD/srh
Enclosure

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Heidi A. Wallace
(Name of Depositor)

PLEASE SIGN AND RETURN TO ACKNOWLEDGE	
Title <i>Linear Full-Rate Phase Detection and Clock...</i>	
Ser/Pat/Reg No: 091784,419	
Filed/Issued: 2-15-01	
<input type="checkbox"/> Assigned Enclosed (List Assignee)	
Client ID: 1100	
Case No: 10987	
Atty/Sec: DPD	
Date Mailed:	2-19-01
Date Due:	10/17/01
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50987-DBP/B600

PATENT

ART UNIT 2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No. 30/DBP/B600

**SUBSTITUTION OF ATTORNEY WITH
CHANGE OF ADDRESS FOR CORRESPONDENCE
BY ASSIGNEE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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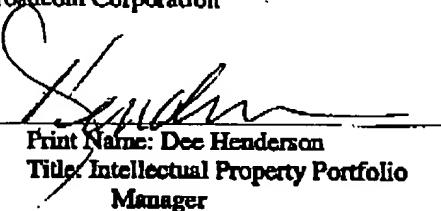
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Please address all correspondence to CHRISTIE, PARKER & HALE, LLP, P.O. Box 7068, Pasadena, California 91109-7068. Customer Number: 23363

Broadcom Corporation

By



Print Name: Dee Henderson
Title: Intellectual Property Portfolio Manager

DBP/djp

DJP PASS24724.1-4-09/5/03 9:47 AM

CPH/BP No. Application No. Filing Date Title

/BP1646	09/614,308	7/12/2000	<u>Fast Acquisition Phase Locked Loop Using a Current DAC</u>
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EXHIBIT A

Page 1

CPH/BP No.

Application No.

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EXHIBIT A

Page 2